

ANSWER KEY

SECOND YEAR HIGHER SECONDARY EXAMINATION MARCH 2023

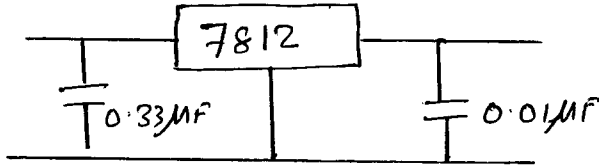
PART-I/H/III

SUBJECT: ELECTRONICS

CODE NO: SY 231 531

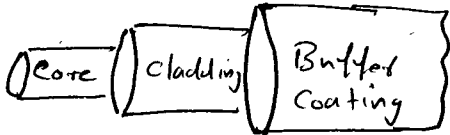
VERSION: S

60 SCORES2 HOURS

Qn. No	Sub Qns	Answer Key/Value Points	Score	Total Score
1		Positive	1	1
2		T Flip Flop	1	1
3		$m = \frac{V_m}{V_c}$	1	1
4		Hertz	1	1
5		Name of any one optical receiver	1	1
6		4:3	1	1
7		1024	1	1
8		Hyper Text transfer Protocol	1	1
9		21	1	1
10			2	2
11		Circuit diagram of -ve clamped output wave form	1 1	2

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Qn. No.	Sub Qns	Answer Key/Value Points	Score	Total Score
12		Logic circuit of S-R Flip Flop	2	2
13		Any two points on need for modulation	1 each	2
14		3-30 MHz — High frequency HF 300-3000 kHz — Medium Frequency MF 3-30 GHz — Super High frequency SHF 30-300 MHz — Very High Frequency VHF	$\frac{1}{d}$ $\frac{1}{d}$ $\frac{1}{d}$ $\frac{1}{d}$	2
15		Any two points on quantization	1 each	2
16		Any two advantages of optical fiber communication	1 each	2
17		Interlaced scanning Process	2	2
18		Block diagram of basic computer systems	2	2
19		Any two advantages of computer networking	1 each	2
20		Any two point on frequency re-use	1 each	2
21		Block diagram of regulated power supply	3	3
22	(a)	Circuit diagram of a differentiator	2	3
	(b)	Spikes	1	
23		Circuit diagram of One bit Comparator	3	3

Qn. No.	Sub Qns	Answer Key/Value Points	Score	Total Score
24	(a) (b)	Block diagram of AM Demodulator working	2 1	3
25		working of Time division multiplexing	3	3
26			3	3
27		frequency spectrum	3	3
28		Input - Mouse, keyboard output - Printer, Monitor Memory - CD, Pen drive	1 1 1	3
29	(a) (b)	Ring topology Star topology	1 ⁺ 1 ⁺	3
30		Circuit diagram of Zener voltage regulator working	2 2	4
31	(a) (b)	Circuit diagram of Summing amplifier $V_o = -(V_a + V_b + V_c)$	3 1	4
32		Block diagram of Superheterodyne receiver	4	4

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Qn. N.	Sub Qns	Answer Key/Value Points	Score	Total Score
33	(a)	Any two points on dispersion	2	4
	(b)	Reduces Bandwidths, Intersymbol interference	2	
34		The block diagram of monochrome TV receiver	4	4